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Efficiency Optimization of the High-Power Isolated DC/DC Converters through THD and Losses Reduction in Isolation Transformers

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Abstract. This paper describes a method of improving efficiency of the high-power transformer-isolated DC/DC converters by means of proper selection of an inverter switch duty cycle. The paper revises old suggestions and recommendations for choosing the maximal duty cycle value on the basis of physical limits imposed by new solid-state devices. The resulting improved efficiency of the converter is described considering losses in separate energy conversion stages.

Keywords

DC/DC converters, efficiency, isolation transformer, inverter.

1. Introduction

High-power transformer-isolated DC/DC converters have a wide scope of applications. The most challenging area is the rolling stock, where the DC/DC converter topologies are mostly used in auxiliary power supplies (APS). In general, APS is the power interface between the traction catenary and the vehicle onboard low-voltage consumers. The auxiliary power supply is directly connected to the traction catenary, which means that it must be fully compliant with specific catenary properties; the large supply voltage swing being one of those. The continuous failure-free operation of the APS must be guaranteed within the following limits of the supply voltage (in DC traction systems):

$$0.67U_{in,nom} \le U_{in} \le 1.3U_{in,nom} \tag{1}$$

where $U_{in,nom}$ is the standardized nominal value of the traction catenary. Table I provides the visual evaluation of the lower and upper limits of different traction catenaries specified by the standard EN50163 [1].

 TABLE I. - Standardized Catenary Voltage Fluctuations

NOMINAL	CONTINUOUS	CONTINUOUS
VOLTAGE	MINIMAL	MAXIMAL
$U_{in,nom}$	VOLTAGE Uin, min	VOLTAGE Uin, max
600	400	770
750	500	950
1500	1000	1950
3000	2000	3900

In the rolling stock applications, the output voltage at the rated load should not change over the full range of the input voltage fluctuations. Based on the accepted voltage fluctuations (Table I), the line regulation (LR) requirement for the rolling stock APS supplied from the traction catenary can be formulated as follows:

$$LR (\%) = \frac{U_{O(Uin, \max)} - U_{O(Uin, \min)}}{U_{O(Uin, nom)}} \cdot 100\% = 0\% .$$
(2)

where U_0 is the output voltage of the converter. Thus, zero-percent line regulation must be considered in the designing and dimensioning of the APS converter for the rolling stock [2].

2. Inverter-Transformer Assembly: Design and Operation

The main specific feature of the APS for the rolling stock is that its input and output stages need to be galvanically isolated. The requirement for safety isolation depends on the integrity of the interconnections between the output of the power supply and the safety isolation provided by the load. Reference should be made to the European standards EN60950 and EN50155. In view of these requirements, a simplified block-diagram of a typical DC/DC auxiliary converter for the DC-fed rolling stock can be proposed (Fig. 1).



Fig. 1. The required structure of the onboard APS converter for the DC-fed rolling stock

The isolation transformer and the zero-percent line regulation required are the main challenges for the designer. Such multistage energy transfer (Fig. 1) is always associated with high losses and lack of efficiency of the developed converter. The most emphasized part here is the inverter-transformer assembly: its operation must be precisely coordinated to achieve higher efficiency and flexibility of the whole converter.

For the described application, the half- and full-bridge isolated DC/DC converter topologies are most feasible: despite relative simplicity, they can provide galvanically isolated output voltage with good regulation properties and with reduced voltage stress on the primary switching devices. This research mostly covers the half-bridge topology based the APS converter (Fig. 2) developed for the 3 kV DC rolling stock [3]. General specifications are presented in Table II.

TABLE II. - Technical Specifications of the Experimental Converter

INVERTER STAGE			
Continuous minimal input voltage $U_{in,min}$, VDC	2000		
Continuous maximal input voltage $U_{in,max}$, VDC	3900		
Nominal input voltage U _{in,nom} , VDC	3000		
Switching frequency f_{sw} , kHz	1		
Type of semiconductor switches	Infineon 6.5 kV 200 A IGBT		
ISOLATION TRANSFORMER STAGE			
Core type of an isolation transformer	Gammamet toroidal core GM14DC		
Operating flux density B_m , T	0.6		
Ambient temperature T_{amb} , °C	70		
Temperature rise ΔT , °C	50		
Wiring material	PURALIT [®] litz		
MISCELLANEOUS			
Converter output voltage U_O , VDC	350		
Desired output power P_{O} , kW	50		

The idea of the control of half-bridge converter operating under the large input voltage swing is to maintain the constant volt-seconds applied to the transformer primary winding (Fig. 3). In other words, the relation $U_{in}D$ (where U_{in} is the input voltage and $D=t_{on}/T_{sw}$ is the transistor duty cycle) should be constant in all the operating points of the converter [4]. To compensate the input voltage variation, the duty cycle of the inverter switch should change inversely proportional to U_{in} to maintain a constant output voltage, U_O :

$$U_{out} = \frac{U_{in} \cdot D}{n}, \qquad (3)$$

where n is the turns ratio of the isolation transformer (power losses have been neglected). To achieve a good design, the operation of the inverter-transformer assembly should be investigated and analyzed in two most demanding operating points: at minimum input voltage, where the switch duty cycle is approaching its predefined maximum D_{max} and at maximum input voltage, where the duty cycle becomes minimal (D_{min}) . These two operating points could be set as boundary operating points. The duty cycle variation range of any isolated DC/DC converter with a large input voltage swing is always lying between these two boundaries. Another important operating point is the nominal operating point, which corresponds to the nominal operating voltage and could be used for the estimation of the efficiency of the power converter.



Fig. 2. Simplified scheme of the experimental converter. The required structure of the onboard APS converter for the DC-fed rolling stock



Fig. 3. Timing diagrams of the inverter-transformer assembly of the half-bridge DC/DC converter

At a fixed switching frequency and with normal steadystate operation, the volt-seconds applied to the transformer windings are constant, independent of the input voltage and load current:

$$U_{in} \cdot t_{on} = \frac{U_{in} \cdot D}{f_{sw}} = \frac{n \cdot U_O}{f_{sw}}, \qquad (4)$$

where U_{in} is the input voltage, t_{on} is the switch on-state time, D is the switch duty cycle, U_O is the output voltage, n is the isolation transformer turns ratio, and f_{sw} is the switching frequency.

3. Selection of the Duty Cycle Variation Range

The selection procedure of the duty cycle variation range should always begin from the definition of the maximum switch duty cycle corresponding to the minimal input voltage. According to conservative design practice for the full- and half-bridge converters, several unified values of the maximum switch duty cycle, such as $D_{max}=0.45$ [5] or even $D_{max}=0.4$, are used [4]. This is done mostly to provide a proper safety margin, thus preventing a short circuit across the supply voltage, which can cause the destruction of power transistors. The minimum duty cycle value D_{min} in power converters with the large input voltage swing is generally selected using (5).

$$D_{\min} = \frac{U_{in,\min}}{U_{in,\max}} \cdot D_{\max} \,. \tag{5}$$

The conservative selection procedure of the duty cycle based on predefined D_{max} values is not always useful because it limits the flexibility and even can reduce the efficiency of the inverter-transformer assembly. The selection of D_{max} should be individually performed in respect to the specific properties of the converter, such as converter topology, application field, transistor types, isolation transformer specifications, etc. This results in the influence on the dynamic properties of switching transistors.

An idea – shifting of D_{max} towards its absolute maximum

To improve the efficiency and flexibility of the highpower half- and full-bridge isolated DC/DC converters, the authors propose to shift the maximum switch duty cycle D_{max} towards its absolute maximum as close as possible.

Theoretically, in the investigated transformer-isolated half-bridge DC/DC converter, the absolute maximum duty cycle can approach 0.5 (switch on-state time $t_{on,lim}$ is half the switching period T_{sw}) but in practice it is always limited by some certain value D_{lim} , which can be expressed as

$$D_{\rm lim} = \frac{t_{on,\rm lim} - t_{IDT}}{T_{sw}} , \qquad (6)$$

where t_{IDT} is the interlock delay time between switching on and off the opposite arm transistors to prevent cross conduction followed by the short circuit of the DC-link and damage of power transistors. To calculate the interlock delay time, the following equation could be used:

$$_{IDT} = \begin{bmatrix} \left(t_{d_{off},\max} - t_{d_{off},\min} \right) + \left(t_{f,\max} - t_{r,\min} \right) + \\ + \left(t_{PDD,\max} - t_{PDD,\min} \right) \end{bmatrix} \cdot SF, (7)$$

t

where $t_{d-off,max}$ and $t_{d-on,min}$ are the maximal turn-off and minimal turn-on delay times of the switching transistor, respectively; $t_{f,max}$ and $t_{r,min}$ are the maximal fall time and minimal rise time of the switching transistor; $t_{PDD,max}$ and t_{PDD,min} are the maximal and minimal signal propagation delays from the control microprocessor to the switching transistor and SF is the safety factor. In (6) the first four terms describe the characteristics of the switching transistor itself plus the gate resistor that is used. The last two terms are the propagation delay time difference determined by the control signal propagation chain from the control circuitry via the gate driver to the switching transistor. Often the interlock delay time is calculated from typical datasheet values just multiplying by the safety factor from field experience. For instance, [6] proposes to use the safety factor SF=1.2. The discussed experimental device is based on 6.5 kV 200 A IGBTs FZ200R65KF1 with dedicated drivers controlled via fiber optics from a high-performance microcontroller, the interlock delay time will be about $t_{IDT}=9$ us, resulting in the absolute maximum duty cycle $D_{lim}=0.49$ at the switching frequency f_{sw} =1 kHz.

Evidently, such procedure demands special accuracy in ensuring correct IDT, which must be done for each application in particular. Thus, in the discussed application with $t_{IDT}=9$ us the proposed maximum switch duty cycle will be $D_{max} = D_{lim} = 0.49$. The selection of the minimum duty cycle value D_{min} should be done by (5).

4. Impact of Different Duty Cycle Variation Ranges on the Efficiency of the Inverter-Transformer Assembly

To verify how the selection of the duty cycle variation range can affect the efficiency of the inverter-transformer assembly of the high-voltage IGBT-based half-bridge DC/DC converter, a series of experiments were performed. The duty cycle variation range was calculated in accordance with the input voltage boundaries presented in Table II. The first two ranges A and B were selected upon the standard procedure with earlier discussed unified maximum duty cycle values 0.4 and 0.45, correspondingly. The selection of the third duty cycle variation range C was based on the proposed design improvement procedure. The investigated duty cycle variation ranges are presented in Table III.

TABLE III. - Duty Cycle Variation Ranges Used in the Analysis

CASE STUDY	D_{max} $(U_{in,max}=$ 2000 V)	D_{nom} $(U_{in,nom} = 3000 \text{ V})$	D_{min} $(U_{in,max} = 3900 \text{ V})$
Α	0.4	0.27	0.21
В	0.45	0.3	0.23
С	0.49	0.33	0.25

Further, the efficiency of the half-bridge inverter and the isolation transformer will be analyzed and compared for these three operating points with similar conditions (see Table I for details).

A. Operation Conditions and Losses of a Half-Bridge Inverter

With the duty cycle being shifted towards maximum, the rms voltage applied to the transformer's primary winding will be increased due to the longer conduction time of the IGBT. For the same transferred power, the rms value of the primary current should proportionally decrease. As a consequence, the rms current of the switching transistors will be decreased, thus having a positive influence on the inverter losses. The comparison of the rms currents of the switching transistor for the different operating voltages and duty cycle values is presented in Table IV.

TABLE IV Impact of the Duty Cycle Variation Range on the
Operating Values of the Inverter Switches

OPERATING	$U_{in,min}$	$U_{in,nom}$	$U_{in,max}$		
POINT	(2000 V)	(3000 V)	(3900 V)		
CASE STUDY A					
Switch duty cycle D	0.4	0.27	0.21		
Switch average current, $I_{c,av}(A)$	25	16.7	12.8		
Switch rms current, $I_{c,rms}$ (A)	39.5	32.1	28.0		
CASE STUDY B					
Switch duty cycle D	0.45	0.3	0.23		
Switch average current, $I_{c,av}(A)$	25	16.7	12.8		
Switch rms current, $I_{c,rms}$ (A)	37.3	30.4	26.7		
CASE STUDY C					
Switch duty cycle D	0.49	0.33	0.25		
Switch average current, $I_{c,av}(A)$	25	16.7	12.8		
Switch rms current, $I_{c,rms}$ (A)	35.7	29.0	25.6		



Maximal input voltage - minimum duty cycle: U_{in,max}D_{min}
 Nomimal input voltage - nominal duty cycle: U_{in,nom}D_{nom}
 Minimal input voltage - maximum duty cycle: U_{in,min}D_{max}

Fig. 4. Average losses of half-bridge inverter operating with different switch duty cycles and input voltages

The analysis (Fig. 4) shows that on an increase of the switch duty cycle at the maximal input voltage by 22.5%, the total losses of the half-bridge inverter could be reduced by 8.1%. The proportional increase of the duty cycle insures a 7.5% and 6.3% loss reduction at the nominal and minimal input voltages, respectively. Although the presented loss reduction increases the efficiency of the inverter only by some fractions of percent, the cooling effort could be reduced, thus resulting in a more space-saving design.

B. Operation Conditions and Efficiency of the Isolation Transformer

The shifting of D_{max} towards its absolute maximum will be followed by the decrease of the amplitude value of the transformer secondary voltage. Neglecting losses in the rectifier diodes and output filter components, the transformer secondary amplitude voltage could be estimated by (8).

$$U_{\text{sec,amp}} = \frac{U_O}{2 \cdot D} , \qquad (8)$$

where U_O is the converter output voltage. Table V shows the impact of the duty cycle variation range change on the main specifications of the isolation transformer.

It is obvious that due to shifting of D_{max} towards its absolute maximum, the turns number of the transformer will also change. Due to an increased duty cycle, the amplitude value of the transformer secondary voltage will be reduced, thus decreasing voltage stress on the rectifier diodes. During the analysis it was assumed that the general specifications of the isolation transformer (core type, volume, operating flux density, ambient temperature and temperature rise, cross-section and type of winding wires, etc. as well as input voltage swing and operating frequency) remain the same for all the investigated cases (see Table I).

Further, based on the data presented in Tables I and V, three different isolation transformers were evaluated and compared for the whole operation range in terms of losses and efficiency. Cases *A*, *B* and *C* correspond to the transformers designed to operate at D_{max} values of 0.4, 0.45 and 0.49, respectively. Each transformer operation was analyzed in three operating points – at U_{min} , U_{nom} and U_{min} with the corresponding D_{min} , D_{nom} and D_{max} . The comparison between cases *A*, *B* and *C* at those operating points is demonstrated below in Figs. 8-12.

It should be mentioned that due to duty cycle changes, to match the input voltage, the converter assembly encounters variable harmonic content. The square wave pulses (Fig. 1) used in the DC/DC converter can be presented as a fundamental component and a set of higher harmonic components by applying the Fourier transformation

$$u(t) = \frac{4 \cdot U_{pr,amp}}{\pi} \sum_{n=1,3,5}^{\infty} \frac{1}{N_n} \sin N_n D\pi \cdot \cos N_n \omega t , \quad (9)$$

where $U_{pr,amp}$ is the amplitude value of the square wave primary voltage of the isolation transformer and D is the inverter switch duty cycle corresponding to the voltage.

TABLE V. - Impact of the Duty Cycle Variation Range on the Specifications of the Isolation Transformer

OPERATING	$U_{in,min}$	$U_{in,nom}$	U _{in,max}	
POINT	(2000 V)	(3000 V)	(3900 V)	
(CASE STUDY	ζ Α		
Switch duty cycle D	0.4	0.27	0.21	
Primary amplitude voltage $U_{pr,amp}$ (V)	1000	1500	1950	
Primary rms voltage $U_{pr,rms}$ (V)	894.4	1102	1264	
Primary rms current $I_{pr,rms}(A)$	55.9	45.4	39.6	
Second. amplitude voltage $U_{sec,amp}$ (V)	437.5	648.2	833.3	
Secondary rms voltage $U_{sec,rms}(V)$	390.6	481.2	551.9	
Secondary rms current $I_{sec,rms}(A)$	128	103.9	90.7	
Turns ratio		2.29		
Primary turns		41		
Secondary turns		18		
(CASE STUDY	Ϋ́B		
Switch duty cycle D	0.45	0.3	0.23	
Primary amplitude voltage U _{pr amp} (V)	1000	1500	1950	
Primary rms voltage $U_{pr,rms}(V)$	948.7	1162	1323	
Primary rms current $I_{nr.rms}(A)$	52.7	43	37.8	
Second. amplitude voltage $U_{sec,amp}$ (V)	388.9	583.3	760.9	
Secondary rms voltage Usec.rms (V)	369.1	452.1	514.8	
Secondary rms current <i>I_{sec.rms}</i> (A)	135.5	110.5	97.1	
Turns ratio		2.57		
Primary turns	42			
Secondary turns		16		
(CASE STUDY	Υ C		
Switch duty cycle D	0.49	0.33	0.25	
Primary amplitude voltage $U_{pr,amp}$ (V)	1000	1500	1950	
Primary rms voltage $U_{pr,rms}$ (V)	990	1219	1379	
Primary rms current I _{pr.rms} (A)	50.5	41	36.3	
Second. amplitude voltage $U_{sec,amp}$ (V)	357.1	535.7	696.4	
Secondary rms voltage $U_{sec.rms}$ (V)	353.6	345.4	492.5	
Secondary rms current $I_{sec rms}$ (A)	141.4	114.8	101.6	
Turns ratio		2.8	I	
Primary turns		42		
Secondary turns		15		

Assuming a lossless converter with a purely resistive load, we can consider the secondary voltage, the primary and secondary current and the primary and secondary powers to be of the same harmonic content. Then the total harmonic distortion (THD) could be defined as the ratio of the sum of the powers of all harmonic components to the power of the fundamental harmonic:

$$THD = \frac{\sum_{n=1,3,5}^{\infty} P_n}{P_1}.$$
 (10)

In the considered cases, equations (9) and (10) take into account odd harmonics up to 49th order.

Fig. 5 shows the dependence of THD on the duty cycle, estimated for the full duty cycle variation range theoretically available for the half-bridge inverter. This figure helps us to understand the plot presented in Fig. 6. The latter shows the transformer power at the fundamental and higher harmonics. As can be seen, at different operating points, the relationship between the powers transferred at the fundamental and higher frequencies can vary according to the changes in THD.



Fig. 5. THD as a function of inverter switch duty cycle



Pow er at fundamental frequency, operating point Umax, Dmin
 Pow er at fundamental frequency, operating point Unom, Dnom
 Pow er at fundamental frequency, operating point Umin, Dmax
 Pow er at higher frequencies, operating point Umax, Dmin
 * Pow er at higher frequencies, operating point Unom, Dnom
 * Pow er at higher frequencies, operating point Unom, Dnom

Fig. 6. Power transferred through isolation transformer at the fundamental and higher frequencies in case studies *A*, *B* and *C*, respectively

Fig. 7 demonstrates the total losses of the three transformers split into losses at the fundamental and higher frequencies. As can be seen, the total power losses decrease at any voltage if an isolation transformer is designed to operate with a higher maximal duty cycle

value. The fundamental losses can increase or decrease depending on the fundamental component of the power at the chosen duty cycle. But the power losses at higher frequencies always decrease even at minimal operational voltage and maximal duty cycle value while the higher maximal duty cycle value is being chosen to design a DC/DC converter.



Fig. 7. Power losses in an isolation transformer: total, at the fundamental and higher frequencies in case studies *A*, *B* and *C*, respectively



- —∆— Additional loss factor for primary winding, operating point Unom, Dnom
- Additional loss factor for primary winding, operating point Umin, Dmax
- —O— Additional loss factor for secondary winding, operating point Umax, Dmin
- Additional loss factor for secondary winding, operating point Unom, Dnom
- Additional loss factor for secondary winding, operating point Umin, Dmax
- Fig. 8. Harmonic-loss factors for primary and secondary windings in case studies *A*, *B* and *C*, respectively

That phenomenon can be explained by the following facts. The optimal transformer design rule of the copper and core loss equalization will not work for middle-frequency (500...2000 Hz) high-power transformers,

because the copper losses are dominating. They are usually more than 90% from the total losses of the transformer. Copper losses can approximately be expressed as follows:

$$P_{copper} = I_{pr,rms}^2 \cdot R_{pr} \cdot F_{AL1} + I_{sec,rms}^2 \cdot R_{sec} \cdot F_{AL2}, (11)$$

where R_{pr} and R_{sec} are the resistances of the winding materials, F_{AL1} and F_{AL2} are the additional loss factors taking into account the rise of AC resistance compared to the DC resistance in the primary and secondary windings. As seen from Fig. 8, the harmonic loss factors are overcoming the rise of THD if the converter operates under high duty cycle and low voltage values (curve for the operating point U_{min} , D_{max} in Fig. 8). As a result, the isolation transformers designed for the higher maximal duty cycle value demonstrate higher efficiency at any operational voltage and duty cycle (Fig. 9).



Fig. 9. Transformer efficiency in case studies *A*, *B* and *C*, respectively



Fig. 10. Comparison of the efficiency of different isolation transformer designs for the same operating voltage range

Fig. 10 shows the comparison of the efficiency of the discussed isolation transformer designs estimated for the whole primary voltage range. The transformer designed with the highest maximal duty cycle (case study *C*) features about 0.3% efficiency rise compared to the conventional one (case study *A*). In the case of other transformer designs operating at higher temperatures, the energy efficiency improvement was of 1...2%, resulting in 0.5...1 kW less power dissipation of the 50 kVA isolation transformer. If the isolation transformer was

designed with very low losses, e.g. efficiency was near 99.5%, then the impact of proper selection of the nominal duty cycle on the transformer efficiency was negligible (reduction of losses was less than 15 W).

5. Conclusions

In this paper, efficiency improvements of the highvoltage isolated DC/DC converter by means of the improved selection of the variation range of the duty cycle of the inverter switch are discussed. A new extended approach of the inverter duty cycle selection is proposed. To verify the proposals, the efficiency of the inverter and isolation transformer was estimated and compared for three different duty cycle variation ranges.

It was found that the new method provides an efficiency rise of the high-voltage half-bridge isolated DC/DC converter. In the case of the investigated rolling stock auxiliary power supply with the rated power of 50 kW even moderate efficiency improvement in 1% will be followed by the 0.5 kW...1 kW smaller heat dissipation, thus resulting in a reduced cooling effort and, therefore, higher power density of the designed converter.

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