

A DC-DC Converter using the Renewable Energies for Battery Charger

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Abstract. With a development of Hybrid Electric Vehicle (HEV), a photovoltaic(PV) generation system is used for charging batteries in many cases. A dc/dc converter using PV power for a battery charger requires a high efficiency. In this paper, A zero voltage switching (ZVS) boost converter using the renewable energies for HEV charger is proposed. Through the theoretical analysis and experimental result, operation modes and characteristics of the proposed topology are verified.

Key words

Battery charger, soft switching, boost converter.

1. Introduction

An energy crisis has become a global issue due to its scarcity and high rocketing price of fossil energy resources. And CO₂ emission has stabilized and the temperature has increased. For these reasons, renewable energy and Hybrid Electric Vehicle (HEV) have been brought to public attention, and a considerable number of researches have been conducted on these fields [1]. In general, HEV is composed of the battery pack, the traction motor, the inverter and the engine [2]. The capacity of the batteries is one of the determining factors of HEV mileage [3]. So it is important that the battery is charged enough. Therefore, a dc/dc converter is needed for the battery energy storage system. Also, if renewable energy is used for charging the car during the car is parked, the electric charges can be saved.

In this paper, a ZVS boost converter using renewable energies for HEV charger station is proposed. The proposed system contains a Maximum Power Point Tracking (MPPT) algorithm [4] and a Constant Current-Constant Voltage(CC-CV) control method[5]. By using a resonance circuit, the proposed topology can reduce the switching loss because the switch is turned on and off with zero voltage switching (ZVS). The proposed topology is verified through simulation and experimental results.

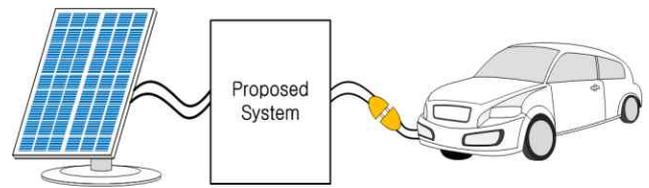


Fig. 1 A schematic of the proposed system

2. Proposed Soft Switching Converter

A. Composition of proposed converter

Fig. 1 shows a schematic of the proposed system. The proposed system transfers a DC power from PV array to batteries of a HEV.

Fig. 2 shows a schematic of the proposed a soft switching boost converter using the PV power for HEV charger station. The proposed converter is based on a conventional boost converter with a soft switching cell that consists of a resonant inductor L_r , a resonant capacitor C_r and a snubber capacitor C_s .

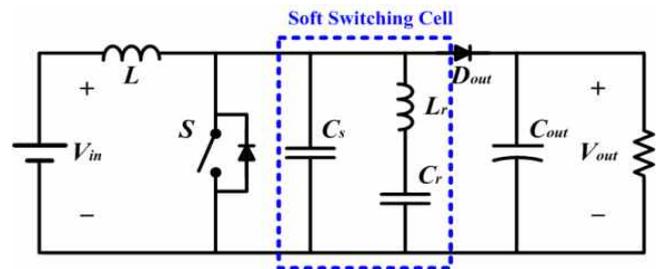


Fig. 2 A schematic of the proposed converter

B. Operation mode

In this section, an operating mode analysis of the proposed converter is performed according to the different current paths of each elements and the voltage

of the switch. The proposed topology is divided into 7 modes. Fig. 3 shows the key waveforms of the proposed converter during a single switching period. Fig. 4 shows the operation modes.

MODE 1 ($t_0 \leq t < t_1$) : Mode 1 starts when the switch is turned on. The main inductor current (i_L) flows to switch and resonant circuit. During this mode, the resonant inductor current (i_{Lr}) decreases to zero. When the resonant capacitor (C_r) is full charged, Mode 1 is finished.

$$i_{Lr}(t) = i_L(t_0) \cos \omega_r t - \frac{v_{Cr}(t_0)}{Z_r} \sin \omega_r t \quad (1)$$

$$v_{Cr}(t) = v_{Cr}(t_0) \cdot \cos \omega_r t + i_L(t_0) \cdot Z_r \sin \omega_r t \quad (2)$$

Where the characteristic impedance (Z_r) and the resonant angular frequency (ω_r) are defined in Eq. (3)

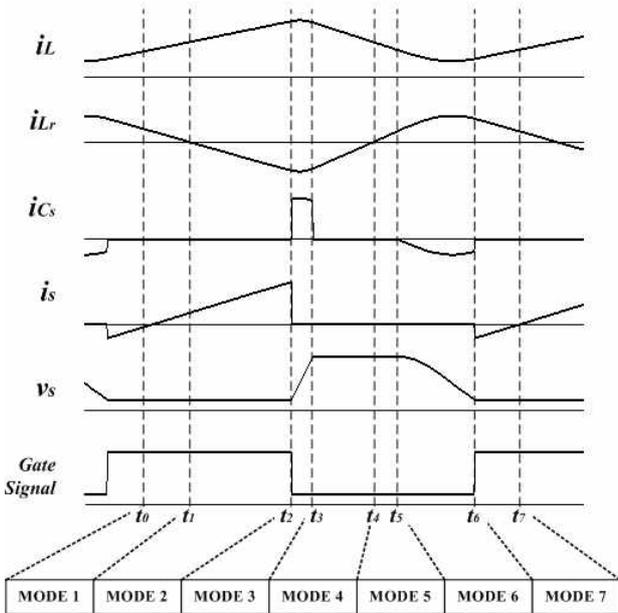


Fig. 3 Key waveforms of the proposed converter

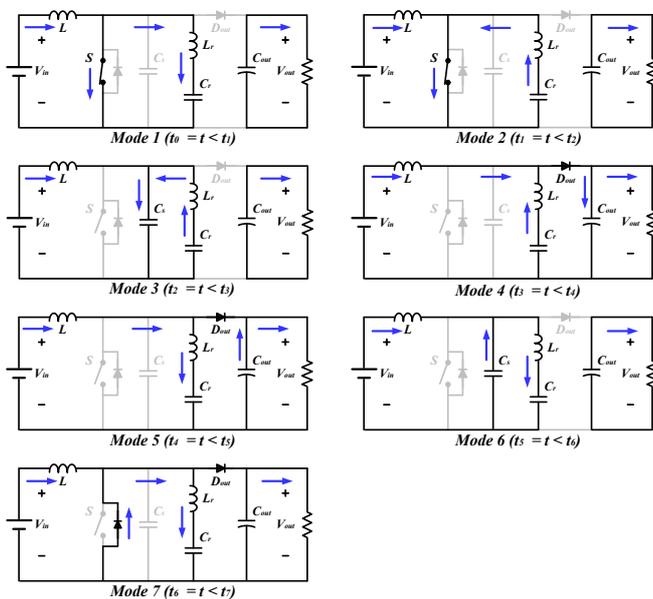


Fig. 4 Operation modes of proposed converter

$$\omega_r = \frac{1}{Z_r}, \quad Z_r = \sqrt{L_r \cdot C_r} \quad (3)$$

MODE 2 ($t_1 \leq t < t_2$) : At time t_1 , the direction of i_{Lr} is changed. i_L and i_{Lr} are added together and the resulting current flows to the switch.

$$i_{Lr}(t) = -\frac{v_{Cr}(t_1)}{Z_r} \sin \omega_r t \quad (4)$$

$$v_{Cr}(t) = v_{Cr}(t_1) \cdot \cos \omega_r t \quad (5)$$

MODE 3 ($t_2 \leq t < t_3$) : This mode is started when the switch S is turned off with ZVS condition, due to the snubber capacitor (C_s). i_L and i_{Lr} flows to C_s together and C_s is charged. When C_s is full charged, mode 3 ends.

$$i_{Lr}(t) = \frac{C}{C_s} i_L(t_2) + \left(i_{Lr}(t_2) - \frac{C}{C_s} i_L(t_2) \right) \cos \omega_a t - \frac{v_{Cr}(t_2)}{Z_a} \sin \omega_a t \quad (6)$$

$$v_{Cr}(t) = \frac{i_L(t_2)}{C_r + C_s} t + \frac{C}{C_s} \cdot v_{Cr}(t_2) \cdot \left(1 + \frac{C_s}{C_r} \cos \omega_a t \right) + \frac{C}{C_r} Z_a \left(i_{Lr}(t_2) - \frac{C}{C_s} i_L(t_2) \right) \sin \omega_a t \quad (7)$$

$$v_{Cs}(t) = \frac{i_L(t_2)}{C_r + C_s} t + \frac{C}{C_s} \cdot v_{Cr}(t_2) \cdot (1 - \cos \omega_a t) + \frac{C}{C_s} Z_a \left(\frac{C}{C_s} i_L(t_2) - i_{Lr}(t_2) \right) \sin \omega_a t \quad (8)$$

$$\text{where } \omega_a = \frac{1}{\sqrt{L_r C}}, \quad Z_a = \sqrt{\frac{L_r}{C}}, \quad C = \frac{C_r C_s}{C_r + C_s}$$

MODE 4 ($t_3 \leq t < t_4$) : At time t_3 , the voltage across C_s is equal to output voltage (V_{out}) and output diode (D_{out}) begins to conduct. The saved energy of the main inductor (L) and the resonant circuit are transmitted to the load through D_{out} . Mode 4 ends when C_r is fully discharged and the direction of i_{Lr} is changed.

$$i_{Lr}(t) = i_{Lr}(t_3) \cdot \cos \omega_r t + \frac{V_o - v_{Cr}(t_3)}{Z_r} \sin \omega_r t \quad (9)$$

$$v_{Cr}(t) = V_o - (V_o - v_{Cr}(t_3)) \cos \omega_r t + i_{Lr}(t_3) \cdot Z_r \sin \omega_r t \quad (10)$$

MODE 5 ($t_4 \leq t < t_5$) : i_L flows to the resonant circuit and the load. i_{Lr} changes the direction. When i_L has become equal to i_{Lr} , this mode is finished.

$$i_{Lr}(t) = \frac{V_o - v_{Cr}(t_4)}{Z_r} \sin \omega_r t \quad (11)$$

$$v_{Cr}(t) = V_o - (V_o - v_{Cr}(t_4)) \cdot \cos \omega_r t \quad (12)$$

MODE 6 ($t_5 \leq t < t_6$) : Mode 6 starts with discharging of C_s . When the voltage across C_s becomes lower than V_{out} , D_{out} turns off and mode 6 ends.

$$i_{Lr}(t) = \frac{C}{C_s} i_L(t_5) \left(1 + \frac{C_s}{C_r} \cos \omega_a t\right) + \frac{V_o - v_{Cr}(t_5)}{Z_a} \sin \omega_a t \quad (13)$$

$$v_{Cr}(t) = \frac{I_L(t_5)}{C_r + C_s} t + v_{Cr}(t_5) + \frac{C}{C_r} (V_o - v_{Cr}(t_5)) (1 - \cos \omega_a t) \quad (14)$$

$$v_{Cs}(t) = \frac{i_L(t_5)}{C_r + C_s} t + \frac{C}{C_r} V_o \left(1 + \frac{C_r}{C_s} \cos \omega_a t\right) + \frac{C}{C_s} \cdot v_{Cr}(t_5) \cdot (1 - \cos \omega_a t) - \frac{C}{C_r + C_s} i_L(t_5) Z_a \sin \omega_a t \quad (15)$$

$$\text{when } C = \frac{C_r C_s}{C_r + C_s}, Z_a = \sqrt{\frac{L_r}{C}}, \omega_a = \frac{1}{\sqrt{L_r C}}$$

MODE 7 ($t_6 \leq t < t_7$) : When i_{Lr} flows to switch's parallel diode with full discharging of C_s , this mode starts. The switch S is turned on with ZVS condition. When switch's parallel diode turns off, mode 7 ends.

$$i_{Lr}(t) = i_{Lr}(t_6) \cos \omega_r t - \frac{v_{Cr}(t_6)}{Z_r} \sin \omega_r t \quad (16)$$

$$v_{Cr}(t) = v_{Cr}(t_6) \cdot \cos \omega_r t + i_{Lr}(t_6) \cdot Z_r \sin \omega_r t$$

3. Simulation

A computer simulation for operational characteristics of proposed dc/dc converter was executed by using PSIM software. The simulation parameters are shown in Table. I. Fig. 5 shows the gate signal, current waveforms, and voltage waveforms of the proposed converter when the input voltage is 80[V]. The output voltage is controlled to DC 400[V].

Fig. 6 shows current and voltage of switch S and Gate signal. The proposed converter is able to perform a ZVS. So it is expected that efficiency of proposed converter is higher than that of a conventional boost converter.

Fig. 7 shows control blocks of proposed system. The proposed system requires a MPPT control for an input power and a CC-CV control for an output power. If control signals of two control methods are mutually exclusive, a

CC-CV control is must be a priority factor.

TABLE I.
SIMULATION PARAMETERS

Parameter	Value
Main inductor L	500 [μ H]
Resonant inductor L_r	100 [μ H]
Snubber capacitor C_s	60 [nF]
Resonant capacitor C_r	2600 [nF]
Output capacitor C_{out}	500 [μ F]
Input voltage	80 [V]
Output voltage	400 [V]
Power	400 [W]

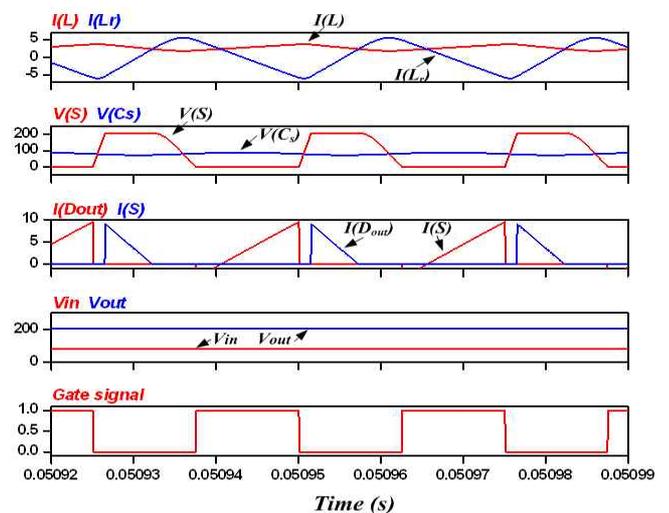


Fig. 5 Current waveforms, voltage waveforms and gate signal of the proposed converter

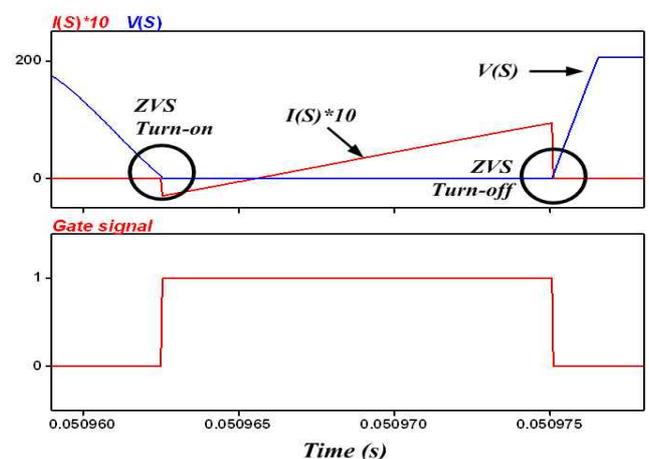


Fig. 6 Current and voltage of switch S

